

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re the Application of:**

Alexander D. Schapira

Serial No.: 09/648,450

Filed: August 28, 2000

Art Unit: 2123

Examiner: Guill, Russell L.

Confirmation No.: 7789

FOR: METHOD AND SYSTEM FOR  
SIMULATION OF ANALOG/DIGITAL  
INTERFACES WITH ANALOG TRI-STATE  
IOPUTS

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants wish to thank the Examiner for allowing the above-identified application.

The Examiner's Reasons for Allowance identifies numerous reasons that may relate to one or more claims. However, Applicants note that the application includes independent claims, where each independent claim may recite limitations that are different from or not present in other claims. As such, the independent claims and their respective dependent claims are allowable for what they recite.

If the Examiner has any questions or comments, the Examiner is respectfully requested to contact the undersigned at the number listed below.

Respectfully submitted,

Dated: December 23, 2009

By: /Peter C. Mei/

Peter C. Mei

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